

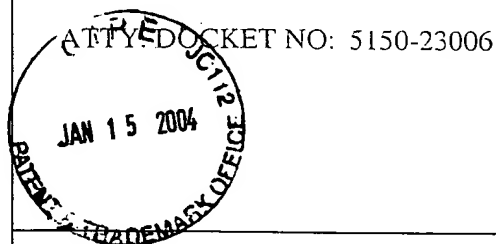
Form PTO-1449 (modified)

List of Patents and Publications

For Applicant's Information

Disclosure Statement

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SERIAL NO: 09/788,219

APPLICANT: Kodosky et al.

FILING DATE: February 16, 2001

GROUP: 2123

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

RF	B1	Choosing Block-Diagram Tools for DSP Design, http://www.bdti.com/articles/info_dspmt95blockdiagram.htm , May 9, 2003, pgs. 1-7.
RF	B2	Real-Time Workshop for Use with Simulink, User's Guide, May 1994, 229 pages.
RF	B3	Guide to Rapid Prototyping with Simulink, Real-Time Workshop and dSPACE, 1995, 16 pages.
RF	B4	Real-Time Interface to Simulink, RTI 30, User's Guide, 1995, 125 pages.
RF	B5	Kevin J Gorman and Kourosh J. Rahnamai, "Real-Time Data Acquisition and Controls Using MatLAB", 1995, 4 pages.
RF	B6	SPW - MatLAB Co-Simulation Interface Product Data Sheet, 1996, 2 pages.
RF	B7	Signal Processing WorkSystem, MatLAB Interface User's Guide, Oct. 1995, 72 pages.
RF	B8	Alta Group of Cadence Design Systems, Inc., 1995, 34 pages.
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RF	B13	Xanalog Corporation Sales Manual, January 1987, 8 pages.
RF	B14	Available XA-1000 Literature and Its Use, 1986, 2 pages.
RF	B15	Xanalog, XA-1000 Programming ICONS, 1986, 26 pages.
RF	B16	Xanalog's CAE System: The Fastest AT Alive, Mass High Tech, Vol. 4, No. 22, 08/1988, 1 page.
RF	B17	Xanalog The Computer Aided Engineering Workstation Comes to Simulation, Simulation Vol. 47, No. 1, July 1986, 3 pages.
RF	B18	Xanalog RT Real Time Analog and Digital I/O, 10/90, 4 pages.
RF	B19	Xanalog/SC+, 9/90, 4 pages.
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RF	B22	Lee et al., "Gabriel: A Design Environment for Programmable DSPs", 11/7/1988, 13 pages.
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RF	B24	Gabriel 0.7 Overview, 1990, 5 pages.
RF	B25	Joseph T. Buck and Edward A. Lee, "Scheduling Dynamic Dataflow Graphs with Bounded Memory Using the Token Flow Model", http://www.synopsys.com/ , 1995, 4 pages.
RF	B26	Edward A. Lee, "Design Methodology for DSP", 1992, 4 pages.
RF	B27	Pino et al., "Interface Synthesis in Heterogeneous System-Level DSP Design Tools, 05/1996, 4 pages.
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RF	B31	Tool Chest continues to Grow, Electronic Engineering Times, 12/15/1995, 2 pages.
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RF	B33	Asawaree Kalavade and Edward A. Lee, "Hardware/Software Co-Design Using Ptolemy - A Case Study, 09/1992, 18 pages.
RF	B34	Pino et al., "Software Synthesis for DSP Using Ptolemy", 1995, 15 pages.
RF	B35	Vol. 1 - Ptolemy 0.7 User's Manual, 1997, 532 pages.
RF	B36	i-Logix Product Overview, 1996, 52 pages.
RF	B37	Press Release, i-Logix Statemate MAGNUM Supports PCs", 01/31/1997, 2 pages.
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